# **SDM310S**

Intel<sup>®</sup> Smart Display Module (SDM-S) with Intel<sup>®</sup> Celeron<sup>™</sup> Elkhart Lake Processor

**User's Manual** 







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### CAUTION

If you replace wrong batteries, it causes the danger of explosion. It is recommended by the manufacturer that you follow the manufacturer's instructions to only replace the same or equivalent type of battery, and dispose of used ones.

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### **ESD** Precautions

Computer boards have integrated circuits sensitive to static electricity. To prevent chipsets from electrostatic discharge damage, please observe the following precautions:

- Do not remove modules or integrated circuits from their anti-static packaging until you are ready to install them.
- Before holding the module or any integrated circuit, touch an unpainted portion of the system unit chassis for a few seconds. Doing so will help discharge static electricity from your body.
- When handling modules and components, wear a wrist-grounding strap, available from most electronic component stores.

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# Section 1 Introduction



The SDM310S is a new Intel<sup>®</sup> Smart Display Module (SDM-S) supporting the Intel<sup>®</sup> J6412 Elkhart Lake processor. It delivers outstanding system performance and support excellent multiple I/Os such as one Gigabyte Ethernet port and two Type C USB 3.2 ports. SDM310S measures only 100 x 60mm and delivers 4K output with streaming content thru the SDM edge connector.

The smart display module carries 4/8GB LPDDR4 onboard memory and 64/128GB eMMC onboard flash. The cost-effective SDM310S features one standard PCIe and built-in I/O such as USB 3.0, HDMI 1.4, DisplayPort 1.2, Serial TX/RX and I<sup>2</sup>C (thru SDM edge connector). In addition, SDM310S can be built in or externally plugged into a display for maximum integration flexibility to fit into the sleekest all-in-one designs.

## 1.1 Features

- Intel<sup>®</sup> Smart Display Module (Intel<sup>®</sup> SDM) specification
- Intel<sup>®</sup> Celeron<sup>®</sup> processor J6412 (Elkhart Lake)
- 4GB LPDDR4 memory onboard (8GB optional)
- 64GB eMMC onboard (128G optional)
- Supports two 4K 60Hz
- Two Type C (USB 3.2) and one GbE LAN
- Small design for easy integration into slim displays
- Optimized for digital signage, public kiosks, professional monitors, point-of-sale
- Projections for magic mirrors, beside terminals, hospitality and more

#### Introduction

#### 1.2 **Specifications**

### CPU

Intel® Celeron® quad core J6412 2.0~2.6GHz. 

### BIOS

- American Megatrends Inc. BIOS. 64Mbit SPI Flash, DMI, Plug and Play.
- PXE Ethernet Boot ROM; customized default saving features; LPC-free supported; uses SPI type Flash memory.
- System Memory
  - 4GB LPDDR4 3200MHz onboard memory supporting maximum capacity up to 8GB (optional).
- eMMC
  - Support 64/128GB eMMC 5.1 onboard flash.

#### **USB** Interface .

- Two USB 3.2 Gen2 ports in Type C, one of which support DP1.2a on rear I/O.
- One USB 3.2 Gen2 port (through SDM edge connector).

### Graphics

- 1 x Type C DisplayPort 1.2a (4096x2160 @60Hz) (rear I/O)
- 1 x HDMI 2.0b 4096x2160 @60Hz (via SDM edge connector) 1 x DisplayPort 1.2a 4096x2160 @60Hz (via SDM edge connector)

### Ethernet

- One 1 GbE provided by RTL8111H support Wake-on-LAN.
- WatchDog Timer .
  - 1~65536 seconds or minutes up to 65536 levels.

### SDM Edge Connector

- 98-pin golden finger, supporting DP, HDMI, PCI-Express x1, USB 3.2 Gen2, Serial TX/RX, I2C.
- **Power Management** 
  - ACPI (Advanced Configuration and Power Interface). .
- Form Factor
  - 100mm x 60mm.

Introduction

#### **Utilities Supported** 1.3

- •
- •
- •
- Chipset driver Graphics driver TXE driver Serial IO driver •
- Ethernet utility and driver

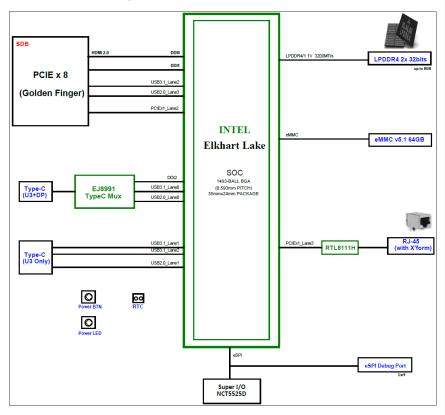


All specifications and images are subject to change without notice.

Introduction

SDM310S Intel<sup>®</sup> Smart Display Module (SDM-S)

## 1.4 Block Diagram

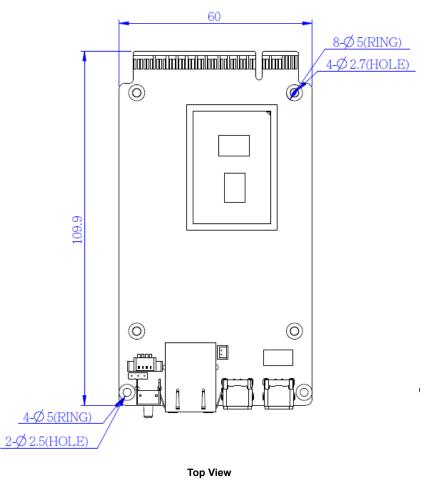


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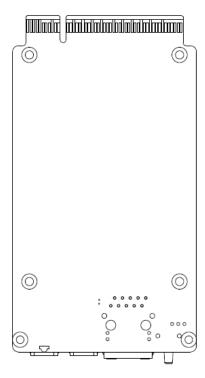
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# Section 2 Module and Pin Assignments

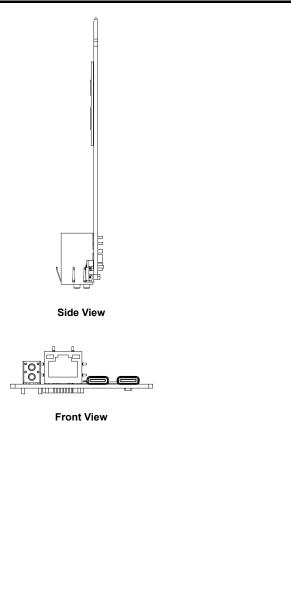
## 2.1 Module Dimensions and Fixing Holes



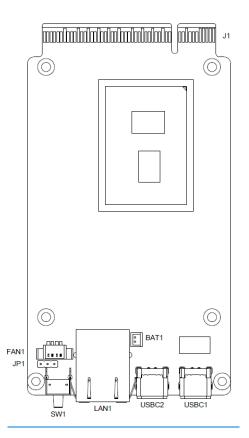
SDM310S Intel<sup>®</sup> Smart Display Module (SDM-S)



Bottom View



## 2.2 Module Layout



Top View

SDM310S Intel® Smart Display Module (SDM-S)

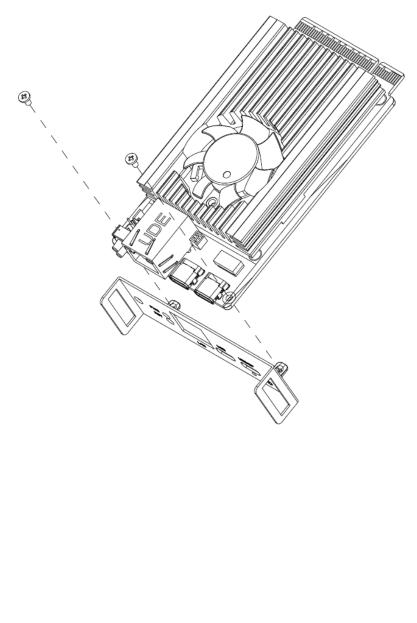


SDM310S Intel<sup>®</sup> Smart Display Module (SDM-S)

### 2.3 Bracket Installation

### 2.3.1 Bracket Assembly

SDM310S bracket assembly.



### 2.4 Jumper Settings

A jumper is a small component consisting of a set of jumper pins with a jumper clip. Place a jumper clip on two jumper pins to close; remove a jumper clip from two jumper pins to open. The following illustration shows how to set up a jumper.

jumper clip



1 2

open

Properly configure jumper settings on the SDM310S to meet your application purpose. Below you can find a summary table of jumpers and their default onboard settings.



Once the default jumper setting needs to be changed, please do it under poweroff condition.

### 2.4.1 Auto Power on (JP1)

If JP1 is enabled for power input, the system will be automatically power on without the need to press the soft power button. If JP1 is disabled for power input, it is necessary to manually press the soft power button to power on the system.

Function	Setting
Enable auto power on (Default)	1-2 close
Disable auto power on	2-3 close



已註解 [jeremy1]: Should it be "JP2"?

### 2.5 Connectors

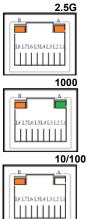
Signals go to the other parts of the system through connectors. Loose or improper connection might cause problems. Please make sure all connectors are properly and firmly connected. Here is a table summarizing the connectors on the hardware.

Connector	Description		
LAN1	2.5G Ethernet Port		
USBC1	Type-C w/ DP Alt		
USBC2	Type-C w/o DP Alt		
SW1	Power and Reset Button		
FAN1	Fan Header		
BAT1	Battery Header		
J1	SDM Edge Connector		

### 2.5.1 Ethernet Port (LAN1)

The board has one RJ-45 Ethernet connector (LAN1). Connection can be established by plugging one end of the Ethernet cable into this connector and the other end (phone jack) to a 2500/1000/100/10-Base-T hub.

MDI0+	
MDI0-	
MDI1+	
MDI1-	
MDI2+	
MDI2-	Г
MDI3+	
MDI3-	
Active Link LED (Orange)	
Off: No link	
Blinking: Data activity detected	
Speed LED	Г
2.5G: Orange	
1000: Green	
100/10: OFF	
	MDI1+ MDI1- MDI2+ MDI2- MDI3- Active Link LED (Orange ) Off: No link Blinking: Data activity detected Speed LED 2.5G: Orange 1000: Green



### 2.5.2 Type-C Connectors (USBC1, USBC2)

The Universal Serial Bus (compliant with USB 3.2 Gen2 (10Gb/s)) connectors are on the rear I/O side. They are both commonly used for installing USB peripherals such as keyboard, mouse, scanner, etc. Only USBC1 also supports 4K display.

Pin	Signal	Pin	Signal
A1	GND	B1	GND
A2	SSTXP1	B2	SSTXP2
A3	SSTXN1	B3	SSTXN2
A4	+5V_VBUS	B4	+5V_VBUS
A5	CC1	B5	CC2
A6	USB_DP0	B6	USB_DP0
A7	USB_DN0	B7	USB_DN0
A8	CSBU1	B8	CSBU2
A9	+5V_VBUS	B9	+5V_VBUS
A10	SSRXN2	B10	SSRXN1
A11	SSRXP2	B11	SSRXP1
A12	GND	B12	GND

Pin	Signal	Pin	Signal
A1	GND	B1	GND
A2	U3_TXP1	B2	U3_TXP2
A3	U3_TXN1	B3	U3_TXN2
A4	+5V_USBC2	B4	+5V_USBC2
A5	A_CC1	B5	A_CC2
A6	USB_DP1	B6	USB_DP1
A7	USB_DN1	B7	USB_DN1
A8	NC	B8	NC
A9	+5V_USBC2	B9	+5V_USBC2
A10	U3_RXN2	B10	U3_RXN1
A11	U3_RXP2	B11	U3_RXP1
A12	GND	B12	GND

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USBC2

SDM310S Intel<sup>®</sup> Smart Display Module (SDM-S)

### 2.5.3 Power and Reset Button (SW1)

The upper power button allows users to control the power on/off of the SDM300S. The lower reset button allows users to reset the SDM300S during abnormal system situations.

Power Button	Description	
Press	Turn on/off system	
Release	Keep system status	



<b>Reset Button</b>	Description	
Press	Reset system	
Release	Keep system status	

### 2.5.4 FAN Header (FAN1)

You can find fan speed option within BIOS Setup Utility if fan is installed. For further information, see BIOS Setup Utility: Advanced\Hardware Monitor\PC Health Status in section 4.4.

Pin	Signal		
1	GND		
2	FAN_DCVOUT		
3	SIO_FANIN		
4	FAN_PWMOUT		

### 2.5.5 RTC Battery Header (BAT1)

This is a header for RTC battery interface.

Pin	Signal
1	VBAT
2	GND

## 2.5.6 SDM Edge Connector (J1)

The following table shows the pin assignments for the 98-pin SDM PCIe x8 edge connector.

Pin	Side B (Top)	Pin	Side A (Bottom)
1	+12V	1	+12V
2	+12V	2	+12V
3	+3.3VSB	3	+12V
4	GND	4	GND
5	GND	5	GND
6	PWRBTN#	6	PWRGD#
7	RESET#	7	SLP S4
8	SYSFAN#	8	SDM DET#
9	GND	9	CEC/NC
10	I2C1_SDA	10	I2C0_SDA
11	I2C1_SCL	11	I2C0_SCL
12	GSPI_MOSI	12	GSPI CLK
13	GSPI_MISO	13	GSPI_CS0#
14	GND	14	GND
15	UART_TXD	15	PCIe_TX+
16	UART_RXD	16	PCIe_TX-
17	GND	17	GND
18	USB_SSTX+	18	PCIe_RX+
19	USB_SSTX-	19	PCIe_RX-
20	GND	20	GND
20	USB_SSRX+	20	PCIe Clk+
21	USB SSRX+	21	PCIe_CIk+ PCIe CIk-
22	GND	22	GND
-	-	-	-
24 25	USB+	24 25	PCIE_WAKE#
	USB-		PCIE_CLKREQ#
26 27	USB_OC# GND	26 27	PCIE_RST#
	-		GND
28 29	DP3- DP3+	28 29	TMDS_CLK-
30	GND	29 30	TMDS_CLK+ GND
	DP2-		
31 32	DP2- DP+	31 32	TMDS0- TMDS0+
32	GND	33	GND
33	DP1-	33	TMDS1-
34	DP1+	35	TMDS1-
	GND		GND
36 37	DP0-	36 37	TMDS2-
37	DP0+	37	TMDS2- TMDS2+
30	GND	30	GND
39 40	DP_AUX-	39 40	DDC_DATA
40	DP_AUX+	40	DDC_DATA
41	DP_HPD	41	TMDS HPD
42	GND	42	GND
43	RSVD	43	RSVD
44 45	RSVD	44 45	RSVD
		45 46	
46 47	RSVD RSVD	46 47	RSVD RSVD
48 49	RSVD	48	RSVD
49	RSVD	49	RSVD

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# Section 3 Hardware Description

### 3.1 Microprocessor

The SMD310S supports Intel<sup>®</sup> Celeron<sup>®</sup> J6412 processors, which enable your system to operate under Windows<sup>®</sup> 10, Windows<sup>®</sup> 11 and Linux environments. The system performance depends on the microprocessor. Make sure all correct settings are arranged for the installed microprocessor to prevent the CPU from damage.

### 3.2 BIOS

The SDM310S uses AMI Plug and Play BIOS with a single 256Mbit SPI Flash.

### 3.3 System Memory

4GB LPDDR4 3200MHz onboard memory supporting maximum capacity up to 8GB.

## 3.4 I/O Port Address Map

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The I/O port address mapping list is shown as follows:

Input/output (IO)
Image: Complex (Complex (Co
[000000000000000020 - 0000000000000021] Programmable interrupt controller
[000000000000024 - 000000000000025] Programmable interrupt controller
[0000000000000028 - 000000000000029] Programmable interrupt controller
[000000000000002C - 0000000000002D] Programmable interrupt controller
[00000000000002E - 000000000002F] Motherboard resources
[0000000000000000000000000000000000000
[000000000000000034 - 0000000000000035] Programmable interrupt controller
[00000000000000033 - 0000000000000039] Programmable interrupt controller
[000000000000000000000000000000000000
[000000000000000000000000000000000000
[000000000000044 - 0000000000043] System time [00000000000044 - 00000000000045] Motherboard resources
[00000000000000000 - 000000000000000000
[000000000000000000000000000000000000
[000000000000000000000000000000000000
[000000000000000000000000000000000000
[000000000000000000000000000000000000
[0000000000000070 - 00000000000000000000
[000000000000000 - 00000000000000] Motherboard resources
[[00000000000092 - 00000000000092] Motherboard resources
[0000000000000000 - 00000000000000000] Programmable interrupt controller
[0000000000000044 - 00000000000A5] Programmable interrupt controller
[0000000000000008 - 0000000000000] Programmable interrupt controller
[000000000000AC - 00000000000AD] Programmable interrupt controller
[0000000000000B0 - 00000000000B1] Programmable interrupt controller
[000000000000B2 - 00000000000B3] Motherboard resources
[000000000000084 - 0000000000085] Programmable interrupt controller
🟣 [0000000000000B8 - 000000000000B9] Programmable interrupt controller
[0000000000000BC - 00000000000BD] Programmable interrupt controller
[0000000000003F8 - 00000000003FF] Communications Port (COM1)
🏣 [00000000000004D0 - 0000000000004D1] Programmable interrupt controller
The second se
Text [0000000000000000 - 000000000000000000
time= [000000000000000000000000000000000000
🏣 [000000000000A20 - 00000000000A2F] Motherboard resources
🏣 [000000000000A30 - 00000000000A3F] Motherboard resources
🏣 [0000000000000A40 - 000000000000A4F] Motherboard resources
🏣 [000000000000000 - 0000000000000000000
🏣 [00000000000164E - 00000000000164F] Motherboard resources
🏣 [000000000001800 - 000000000018FE] Motherboard resources
🏣 [000000000001854 - 000000000001857] Motherboard resources
🏣 [000000000000000 - 000000000000020FE] Motherboard resources
🚅 [0000000000003000 - 0000000000030FF] Realtek PCIe GbE Family Controller
🏣 [0000000000003000 - 00000000003FFF] Intel(R) PCI Express Root Port #6 - 4B3E
🏣 [00000000000004000 - 000000000004FFF] Intel(R) PCI Express Root Port #4 - 4B3C
🏣 [0000000000005000 - 00000000000503F] Intel(R) UHD Graphics
🏣 [00000000000EFA0 - 0000000000EFBF] Intel(R) SMBus Controller - 4B23

### 3.5 Interrupt Controller (IRQ) Map

The interrupt controller (IRQ) mapping list is shown as follows:

 Interrupt request (IRQ) timer (ISA) 0x00000000 (00) System timer (ISA) 0x00000004 (04) Communications Port (COM1) to (ISA) 0x00000037 (55) Microsoft ACPI-Compliant System Tan (ISA) 0x00000038 (56) Microsoft ACPI-Compliant System to (ISA) 0x00000039 (57) Microsoft ACPI-Compliant System [ISA) 0x0000003A (58) Microsoft ACPI-Compliant System 🏣 (ISA) 0x0000003B (59) Microsoft ACPI-Compliant System to (ISA) 0x0000003C (60) Microsoft ACPI-Compliant System ISA) 0x0000003D (61) Microsoft ACPI-Compliant System to (ISA) 0x0000003E (62) Microsoft ACPI-Compliant System ISA) 0x0000003F (63) Microsoft ACPI-Compliant System to (ISA) 0x00000040 (64) Microsoft ACPI-Compliant System III (ISA) 0x00000041 (65) Microsoft ACPI-Compliant System to (ISA) 0x00000042 (66) Microsoft ACPI-Compliant System to (ISA) 0x00000043 (67) Microsoft ACPI-Compliant System to (ISA) 0x00000044 (68) Microsoft ACPI-Compliant System Tan (ISA) 0x00000045 (69) Microsoft ACPI-Compliant System to (ISA) 0x00000046 (70) Microsoft ACPI-Compliant System to (ISA) 0x00000047 (71) Microsoft ACPI-Compliant System to (ISA) 0x00000048 (72) Microsoft ACPI-Compliant System to (ISA) 0x00000049 (73) Microsoft ACPI-Compliant System to (ISA) 0x0000004A (74) Microsoft ACPI-Compliant System La (ISA) 0x0000004B (75) Microsoft ACPI-Compliant System to (ISA) 0x0000004C (76) Microsoft ACPI-Compliant System to (ISA) 0x0000004D (77) Microsoft ACPI-Compliant System ISA) 0x0000004E (78) Microsoft ACPI-Compliant System to (ISA) 0x0000004F (79) Microsoft ACPI-Compliant System to (ISA) 0x00000050 (80) Microsoft ACPI-Compliant System to (ISA) 0x00000051 (81) Microsoft ACPI-Compliant System III (ISA) 0x00000052 (82) Microsoft ACPI-Compliant System to (ISA) 0x00000053 (83) Microsoft ACPI-Compliant System ISA) 0x00000054 (84) Microsoft ACPI-Compliant System to (ISA) 0x00000055 (85) Microsoft ACPI-Compliant System ISA) 0x00000056 (86) Microsoft ACPI-Compliant System to (ISA) 0x00000057 (87) Microsoft ACPI-Compliant System to (ISA) 0x00000058 (88) Microsoft ACPI-Compliant System to (ISA) 0x00000059 (89) Microsoft ACPI-Compliant System E (ISA) 0x0000005A (90) Microsoft ACPI-Compliant System 뻱 (ISA) 0x0000005B (91) Microsoft ACPI-Compliant System [ISA] 0x0000005C (92) Microsoft ACPI-Compliant System to (ISA) 0x0000005D (93) Microsoft ACPI-Compliant System [ISA) 0x0000005E (94) Microsoft ACPI-Compliant System ISA) 0x0000005F (95) Microsoft ACPI-Compliant System to (ISA) 0x00000060 (96) Microsoft ACPI-Compliant System ISA) 0x00000061 (97) Microsoft ACPI-Compliant System to (ISA) 0x00000062 (98) Microsoft ACPI-Compliant System to (ISA) 0x00000063 (99) Microsoft ACPI-Compliant System

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### SDM310S Intel<sup>®</sup> Smart Display Module (SDM-S)

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🏣 (ISA) 0x000001FB (507) Mici	osoft ACPI-Compliant System
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tic (ISA) 0x000001FD (509) Mic	rosoft ACPI-Compliant System
ticr (ISA) 0x000001FE (510) Micr	osoft ACPI-Compliant System
🏣 (ISA) 0x000001FF (511) Micr	osoft ACPI-Compliant System
🏣 (PCI) 0x00000010 (16) High	Definition Audio Controller
(PCI) 0x0000010 (16) Intel 1	SD Host Controller
tel(I) 0xFFFFFF9 (-7) Intel	R) Management Engine Interface #1
🔙 (PCI) 0xFFFFFFA (-6) Intel(	R) UHD Graphics
(PCI) 0xFFFFFFB (-5) Intel(	R) USB 3.10 eXtensible Host Controller - 1.20 (Microsoft)
👮 (PCI) 0xFFFFFFC (-4) Realt	ek PCIe GbE Family Controller
trei(PCI) 0xFFFFFFD (-3) Intel	R) PCI Express Root Port #6 - 4B3E
tel(I) 0xFFFFFFE (-2) Intel	R) PCI Express Root Port #4 - 4B3C

# 3.6 Memory Map

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The memory mapping list is shown as follows:

M M	emory
	[00000000000000000 - 0000000000BFFFF] PCI Express Root Complex
	[0000000000E0000 - 0000000000E3FFF] PCI Express Root Complex
	[0000000000E4000 - 0000000000E7FFF] PCI Express Root Complex
-	[0000000000E8000 - 0000000000EBFFF] PCI Express Root Complex
	[000000000EC000 - 000000000EFFF] PCI Express Root Complex
	[0000000000F0000 - 0000000000FFFFF] PCI Express Root Complex
	[000000007FC00000 - 00000000805FFFF] Intel(R) PCI Express Root Port #4 - 4B3C
_	[000000007FC00000 - 00000000BFFFFFFF] PCI Express Root Complex
	0000000080600000 - 000000080603FFF] Realtek PCIe GbE Family Controller
	[0000000080600000 - 0000000806FFFFF] Intel(R) PCI Express Root Port #6 - 4B3E
_	[000000080604000 - 000000080604FFF] Realtek PCIe GbE Family Controller
	[00000000C0000000 - 0000000CFFFFFF] Motherboard resources
	[0000000FD000000 - 0000000FD68FFFF] Motherboard resources
	[00000000FD6B0000 - 0000000FD6CFFFF] Motherboard resources
	[00000000FD6F0000 - 00000000FDFFFFF] Motherboard resources
	[00000000FE000000 - 00000000FE01FFFF] Motherboard resources
1	[00000000FE010000 - 00000000FE010FFF] Intel(R) SPI (flash) Controller - 4824
1	[00000000FE200000 - 00000000FE7FFFFF] Motherboard resources
	[00000000FEC80000 - 00000000FECFFFF] Motherboard resources
	[00000000FED00000 - 00000000FED003FF] High precision event timer
	[00000000FED20000 - 00000000FED7FFFF] Motherboard resources
_	[00000000FED40000 - 00000000FED44FFF] Trusted Platform Module 2.0
_	[00000000FED45000 - 00000000FED8FFF] Motherboard resources
	[0000000FED90000 - 0000000FED93FFF] Motherboard resources
	[0000000FEDA0000 - 0000000FEDA0FFF] Motherboard resources
	[0000000FEDA1000 - 0000000FEDA1FFF] Motherboard resources
	[0000000FEE00000 - 0000000FEEFFFF] Motherboard resources
	I (0000000FF000000 - 0000000FFFFFFF) Motherboard resources
_	[000000400000000 - 000000400FFFFFF] Intel(R) UHD Graphics
	[0000006000000000 - 00000060009FFFFF] Intel(R) PCI Express Root Port #4 - 4B3C
	[0000006001000000 - 000006001FFFFF] Intel(R) UHD Graphics [000006002100000 - 00000600210FFFF] Intel(R) USB 3.10 eXtensible Host Controller - 1.20 (Microsoft)
	[0000000002100000 - 00000000210FFF] Intel(R) GSB 3.10 Extensible Fost Controller - 1.20 (Wicrosoft)
	[000000002118000 - 000000002118FFF] Intel (N SIMBUS Controller - 4025
	[000000600211C000 - 00000600211CFFF] Intel(B) Serial IO I2C Controller #5 - 4B4C
	[000000600211D000 - 000000600211DFFF] Intel(R) Serial IO I2C Controller #4 - 484B
	[000000600211F000 - 000000600211FFFF] Intel(R) Serial IO I2C Controller #3 - 487B
_	[0000006002120000 - 0000006002120FFF] Intel(R) Serial IO I2C Controller #1 - 4B79
	[0000006002121000 - 0000006002121FFF] Intel(R) Serial IO I2C Controller #0 - 4878
	[0000007FFFEFB000 - 0000007FFFEFBFFF] Intel(R) Management Engine Interface #1
	[0000007FFFEFC000 - 0000007FFFEFFFFF] High Definition Audio Controller
	[0000007FFFF00000 - 0000007FFFFFFFF] High Definition Audio Controller

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# **Section 4 AMI BIOS Setup Utility**

The AMI UEFI BIOS provides users with a built-in setup program to modify basic system configuration. All configured parameters are stored in a flash chip to save the setup information whenever the power is turned off. This section provides users with detailed description about how to set up basic system configuration through the AMI BIOS setup utility.

#### 4.1 Starting

To enter the setup screens, follow the steps below:

- 1.
- Turn on the computer and press the <Del> key immediately. After you press the <Del> key, the main BIOS setup menu displays. You can access the 2 other setup screens, such as the Advanced and Chipset menus, from the main BIOS setup menu.

It is strongly recommended that you should avoid changing the chipset's defaults. Both AMI and your system manufacturer have carefully set up these defaults that provide the best performance and reliability.

#### 4.2 **Navigation Keys**

The BIOS setup/utility uses a key-based navigation system called hot keys. Most of the BIOS setup utility hot keys can be used at any time during the setup navigation process. These keys include <F1>, <F2>, <Enter>, <ESC>, <Arrow> keys, and so on.



Some of the navigation keys differ from one screen to another.

Hot Keys	Description	
→← Left/Right	The Left and Right <arrow> keys allow you to select a setup screen.</arrow>	
∱∳ Up/Down	The Up and Down <arrow> keys allow you to select a setup screen or sub- screen.</arrow>	
+– Plus/Minus	The Plus and Minus <arrow> keys allow you to change the field value of a particular setup item.</arrow>	
Tab	The <tab> key allows you to select setup fields.</tab>	
F1	The <f1> key allows you to display the General Help screen.</f1>	
F2	The <f2> key allows you to load previous values.</f2>	
F3	The <f3> key allows you to load optimized defaults.</f3>	
F4	The <f4> key allows you to save any changes you have made and exit Setup. Press the <f4> key to save your changes.</f4></f4>	
Esc	The <esc> key allows you to discard any changes you have made and exit the Setup. Press the <esc> key to exit the setup without saving your changes.</esc></esc>	
Enter	The <enter> key allows you to display or change the setup option listed for a particular setup item. The <enter> key can also allow you to display the setup sub-screens.</enter></enter>	

### 4.3 Main Menu

The Main BIOS setup screen is the first screen you see when entering the setup utility. You can always return to the Main BIOS setup screen by selecting the Main tab. System Time/Date can be set up as described below. The Main BIOS setup screen is also shown below.

Main Advanced Chipset	Aptio Setup - AMI Security Boot Save & Exit			
BIOS Information Build Date and Time Project Version	07/08/2024 13:47:28 SDM3105 v1.00	Set the Date. Use Tab to switch between Date elements. Default Ranges: Year: 1998-9999		
Firmware Information ME Firmware Version ME Firmware Mode ME Firmware SKU	15.40.26.2619 Normal Mode Consumer SKU	Months: 1-12 Days: Dependent on month Range of Years may vary.		
System Date System Time	[Fri 07/26/2024] [10:15:45]			
Access Level	Administrator	<pre>→+: Select Screen 11: Select Item Enter: Select +/-: Change Opt. F1: General Help F2: Previous Values F3: Optimized Defaults F4: Save &amp; Exit ESC: Exit</pre>		
Version 2.22.1282. Copyright (C) 2024 AMI				

### **BIOS Information**

Display BIOS and ME firmware information.

### System Date/Time

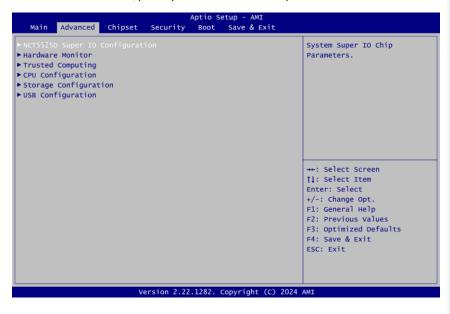
Use this option to change the system time and date. Highlight System Time or System Date using the <Arrow> keys. Enter new values through the keyboard. Press the <Tab> key or the <Arrow> keys to move between fields. The date must be entered in MM/DD/YY format. The time is entered in HH:MM:SS format.

### 4.4 Advanced Menu

The Advanced menu also allows users to set configuration of the CPU and other system devices. You can select any of the items in the left frame of the screen to go to the sub menus:

- NCT5525D Super IO Configuration
- Hardware Monitor
- Trusted Computing
- CPU Configuration
- Storage Configuration
- USB Configuration

For items marked with "▶", please press <Enter> for more options.



已註解 [jeremy2]: The menu item order appears inconsistent with the screenshot below.

## NCT5525D Super IO Configuration

You can use this screen to select options for the Super IO Configuration, and change the value of the selected option. A description of the selected item appears on the right side of the screen. For items marked with " $\blacktriangleright$ ", please press <Enter> for more options.

Advanced	Aptio Setup - AMI	
NCT5525D Super IO Configuration		Set Parameters of Serial Port 1 (COMA).
Super IO Chip ▶ Serial Port 1 Configuration	NCT5525D	
WatchDog	[Disabled]	
		<pre>→+: Select Screen †↓: Select Item</pre>
		Enter: Select +/-: Change Opt.
		F1: General Help F2: Previous Values
		F3: Optimized Defaults F4: Save & Exit
		F4: Save & Exit ESC: Exit
	122 1282 conuninht (c) 2024	

Serial Port 1 Configuration

Use this item to set parameters related to serial port 1.

## WatchDog

Enable or disable WatchDog.



When WatchDog is enabled, the following items will appear: WatchDog Count Mode Use this item to select WatchDog count mode.

WatchDog TimeOut Value Fill in with WatchDog timeout value, where 0 means disabled.

• Serial Port 1 Configuration

Advanced	Aptio Setup - AMI	
Serial Port 1 Configuration		Enable or Disable Serial Port (COM).
Serial Port Device Settings	[Enabled] IQ=3F8h; IRQ=4;	((())).
		++: Select Screen 11: Select Item Enter: Select +/-: Change Opt. F1: General Help F2: Previous Values F3: Optimized Defaults F4: Save & Exit ESC: Exit
Versio	n 2.22.1282. Copyright (C) 2	2024 AMT

Serial Port Enable or disable serial port 1. The optimal setting for base I/O address is 3F8h and for interrupt request line is IRQ4. The default setting is Enabled.

## • Hardware Monitor

This menu allows the user to monitor hardware health status as well as enable or disable the Smart Fan function.

Advanced	Aptio Setup - AMI	
Pc Health Status > Smart Fan function Temperature 1 FD/RPM 1 Temperature 2 FD/RPM 2 Temperature 3 FD/RPM 3 Temperature 4 FD/RPM 4	[Enabled] 35 76 40 127 45 178 50 255	Smart Fan function control Enabled: Smart Fan Mode Disabled: Manual Mode
CPU temperature System temperature Fan Speed +SV VBAT	: +37 °C : +37 °C : 3571 RPM : +4.962 V : +2.992 V	++: Select Screen 11: Select Item Enter: Select +/-: Change Opt. F1: General Help F2: Previous Values F3: Optimized Defaults F4: Save & Exit ESC: Exit
V	ersion 2.22.1282. Copyright (C)	2024 AMI

This screen displays the temperature of system and CPU, fan speed, system voltages (+5V and VBAT).

## Smart Fan function

Enable or disable Smart Fan function.

- Enabled: Select Smart Fan mode.
- Disable: Select manual mode.
- The default setting is Enabled.

## Temperature 1/2/3/4

When system temperature is higher than threshold of Temperature 1/2/3/4, the system fan will spin faster according to FD/RPM 1/2/3/4 setting.

## FD/RPM 1

Default is 76, which means 76/255 = 30% of PWM.

## FD/RPM 2

Default is 127, which means 127/255 = 50% of PWM.

## FD/RPM 3

Default is 178, which means 178/255 = 70% of PWM.

## FD/RPM 4

Default is 255, which means 255/255 = 100% of PWM.

AMI BIOS Setup Utility

已註解 [jeremy3]: Please check the accuracy of this description.

## • Trusted Computing

This menu contains settings related to the Trusted Platform Module (TPM) and other security features.

TPM 2.0 Device Found		Enables or Disables BIOS
Firmware Version:	600.15	support for security device
Vendor:	INTC	O.S. will not show Securit
		Device. TCG EFI protocol a INTIA interface will not b
Active PCR banks	SHA256	available.
Available PCR banks	SHA256,SHA384,SM3	available.
SHA256 PCR Bank	[Enabled]	
SHA384 PCR Bank	[Disabled]	
SM3_256 PCR Bank	[Disabled]	
Pending operation	[None]	
Platform Hierarchy	[Enabled]	++: Select Screen
Storage Hierarchy	[Enabled]	
Endorsement Hierarchy	[Enabled]	11: Select Item
Physical Presence Spec Version	[1.3]	Enter: Select
TPM 2.0 InterfaceType	[AMI CRB Page]	+/-: Change Opt.
Device Select	[Auto]	F1: General Help
		F2: Previous Values
		F3: Optimized Defaults
		F4: Save & Exit
		ESC: Exit

## Security Device Support

Enable or disable BIOS support for security device, typically a TPM. When enabled (default setting), the BIOS initializes and makes the TPM available for use by the operating system and other system components. When disabled, the TPM is effectively hidden from the OS, and critical security functions, such as secure boot and disk encryption, may not function. Additionally, TCG EFI protocols and the INT1A interface, which are used for secure boot and other trusted computing tasks, will not be available.

## SHA256 PCR Bank

Enable or disable SHA256 hashing algorithm for PCR (Platform Configuration Register) Bank within the TPM.

## SHA384 PCR Bank

Enable or disable SHA384 hashing algorithm for PCR Bank within the TPM.

## SM3\_256 PCR Bank

Enable or disable SM3\_256 cryptographic hash algorithm for PCR Bank.

## Pending operation

Schedule an operation for the security device.

- None
- TPM Clear: Clear all data secured by TPM.

Note that your computer will reboot during restart in order to change State of Security Device.

## **Platform Hierarchy**

Enable or disable platform hierarchy, used to manage keys and operations related to the entire platform.

## Storage Hierarchy

Enable or disable storage hierarchy, used to manage keys and operations related to data storage.

## **Endorsement Hierarchy**

Enable or disable endorsement hierarchy, used to manage keys and operations related to the TPM's endorsement keys.

## **Physical Presence Spec Version**

This setting allows you to select the version of the Physical Presence Interface (PPI) Specification: Spec Version 1.2 or 1.3. Note some HCK tests might not support 1.3.

## **Device Select**

This setting determines which version of the TPM is supported and active on your system TPM 1.2: Restrict support to only TPM 1.2 devices.

- -
- TPM 2.0: Restrict support to only TPM 2.0 devices. Auto: This option provides flexibility by supporting both TPM 1.2 and TPM 2.0 devices. The system will default to TPM 2.0 if it is available, and will fall back to TPM 1.2 if -TPM 2.0 is not found.

## **CPU Configuration**

This screen shows the CPU Configuration, and you can change the value of the selected option.

CPU Configuration		When enabled, a VMM can
Туре	Intel(R) Celeron(R) J6412 @ 2.00GHz	utilize the additional hardware capabilities provided by Vanderpool Technology.
ID	0x90661	by funderpoor reclinitionsy)
Speed	2000 MHz	
L1 Data Cache	32 KB x 4	
L1 Instruction Cache	32 KB x 4	
L2 Cache	1536 кв х 4	
L3 Cache	4 MB	
L4 Cache	N/A	
VMX	Supported	
SMX/TXT	Not Supported	
Package C State Limit	[C0/C1]	++-: Select Screen
		<pre> \$\$ \$\$ \$\$ \$\$ \$\$ \$\$ \$\$ \$\$ \$\$ \$\$ \$\$ \$\$ \$\$</pre>
		Enter: Select
Intel(R) Speed Shift Technology	[Disabled]	+/-: Change Opt.
C states	[Disabled]	F1: General Help
		F2: Previous Values
		F3: Optimized Defaults
		F4: Save & Exit
		ESC: Exit
		ESC: EXIL

Intel (VMX) Virtualization Technology Enable or disable Intel Virtualization Technology. When enabled, a VMM (Virtual Machine Mode) can utilize the additional hardware capabilities. It allows a platform to run multiple operating systems and applications independently, hence enabling a single computer system to work as several virtual systems. The default setting is Enabled.

## Intel(R) Speed Shift Technology

Enable or disable Intel® Speed Shift Technology support. Enabling will expose the CPPC v2 interface to allow for hardware controlled P-states.

## **Turbo Mode**

Enable or disable processor Turbo Mode. The processor can be up to maximum turbo frequency when the system loading becomes higher. The default setting is Enabled.

## C states

Enable or disable CPU power management. Allows CPU to go to C states when it's not 100% utilized.

**Storage Configuration** This screen specifies storage information. For items marked with "**>**", please press <Enter> for more options.

Advanced Advanced	o Setup - AMI
Storage Configuration	eMMC Configuration Parameters
▶ eMMC Configuration	
	<pre>→+: Select Screen 1 : Select Item Enter: Select +/-: Change Opt. F1: General Help F2: Previous Values F3: Optimized Defaults F4: Save &amp; Exit ESC: Exit</pre>
Version 2.22.128	2. Copyright (C) 2024 AMI

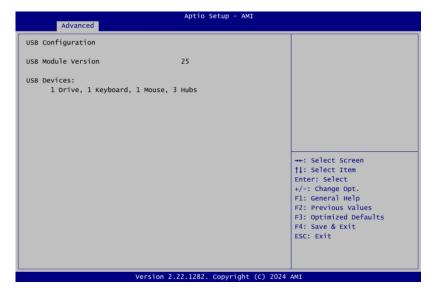
Specifies eMMC storage information as follows:

Advanced		
eMMC Configuration		<b>1</b>
Mass Storage Devices:		
Bus 0 Dev 1A Func 0 eMMC IM064G(62.5GB)	[Auto]	
EMMC 1M004G(02.3GB)	[AUCO]	

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USB Configuration





USB Devices Display all detected USB devices.

## 4.5 Chipset Menu

The Chipset menu shows memory configuration parameters dan graphics configuration version.

Aptio Setup - AMI					
Main Advanced Chipset	Security Boot Save & Exit				
System Agent (SA) Configurat	ion				
Memory RC Version Memory Size Frequency	0.0.4.104 4096 MB 3200 MTPS				
Channel 0 Slot 0 Size Number of Ranks Manufacturer	Populated & Enabled 4096 MB (LPDDR4) 1 Samsung				
Graphics Configuration IGFX GOP Version	18.0.1044	<pre>++: Select Screen  ↑↓: Select Item</pre>			
PCH-IO Configuration		Enter: Select +/-: Change Opt. F1: General Help F2: Previous Values F3: Optimized Defaults F4: Save & Exit ESC: Exit			
	ersion 2.22.1282. Copyright (C) 2024	AMT			

## 4.6 Security Menu

The Security menu allows users to enhance system security by creating an administrator password to limit system access.

	Main	Advanced	Chipset	A		etup - AMI Save & Exit	
	Password	Descriptio	on				Set Administrator Password
	then this only aske If ONLY t is a powe boot or e have Admi The passw	only lim d for when he User's r on passw nter Setup nistrator ord length lowing ra	its access n entering password word and m p. In Setu rights. h must be	password is to Setup ar Setup. is set, ther ust be enter p the User w 3	nd is n this red to		
	Maximum 1	2		20			<pre>++: Select Screen  ↑↓: Select Item Enter: Select</pre>
	User Pass	ator Pass word					+/-: Change Opt. F1: General Help
	► Secure Bo	oot					F2: Previous Values F3: Optimized Defaults F4: Save & Exit ESC: Exit
ĺ			Ve	ersion 2.22.	1282. (	Copyright (C) 2024	4 AMI

## Administrator Password

This item indicates whether an administrator password has been set (installed or uninstalled).

## User Password

This item indicates whether a user password has been set (installed or uninstalled).

## Secure Boot

	Aptio Setup - AMI Security	
System Mode	Setup	Secure Boot feature is Active if Secure Boot is Enabled.
Secure Boot	[Disabled] Not Active	Platform Key(PK) is enrolled and the System is in User mode. The mode change requires
Secure Boot Mode	[Custom]	platform reset
<ul> <li>Restore Factory Keys</li> <li>Reset To Setup Mode</li> </ul>		
►Key Management		
		++: Select Screen
		<pre> fl: Select Item </pre>
		Enter: Select +/-: Change Opt.
		F1: General Help
		F2: Previous Values
		F3: Optimized Defaults F4: Save & Exit
		ESC: Exit
	rsion 2.22.1282. Copyright (	c) 2024 ANT

### Secure Boot

Secure Boot feature is Active if Secure Boot is Enabled, Platform Key (PK) is enrolled and the System is in User mode. The mode change requires platform reset. Secure Boot ensures that the system only boots from trusted software, preventing malicious software from loading and compromising the device. It checks the digital signatures of boot loaders, firmware, and operating systems to verify that they are from trusted sources and have not been tampered with. Users can choose to enable it or not, between standard and custom mode. The default setting is secure boot.

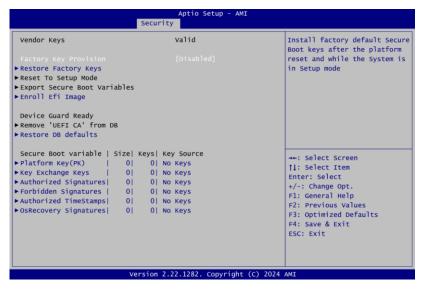
## Secure Boot Mode

Secure Boot mode options: Standard or Custom. In Custom mode, the policy of Secure Boot variables can be configured by a physically present user without full authentication. The default setting is custom.

## **Restore Factory Keys**

Force system to user mode. Install factory default secure boot key databases.

Key Management Enables expert users to modify secure boot policy variables without full authentication.



## **Factory Key Provision**

The Factory Key Provision ensures that the device has secure access to encrypted resources, such as data storage or communication. The keys are often unique to each device and can be used to secure firmware updates, secure boot processes, and to encrypt data at rest. The factory key provisioning process is an important step in securing devices and maintaining the confidentiality of sensitive information.

## 4.7 Boot Menu

The Boot menu allows users to change boot options of the system.

Main Advanced Chipset	Aptio Setup - AMI Security <mark>Boot</mark> Save & Exit	
Boot Configuration Setup Prompt Timeout Bootup Numlock State Quiet Boot Network Stack	3 [On] [Disabled] [Disabled]	Number of seconds to wait for setup activation key. 65535(0xFFFF) means indefinite waiting.
Boot Option Priorities Boot Option #1	[UEFI: TOSHIBA TransMemory 1.00, Partition 1 (TOSHIBA TransMemory 1.00)]	
Boot Option #2	[windows Boot Manager (eMMC IM064G)]	<pre>→+: Select Screen 11: Select Item Enter: Select +/-: Change Opt. F1: General Help F2: Previous Values F3: Optimized Defaults F4: Save &amp; Exit ESC: Exit</pre>
	version 2.22.1282. Copyright (C) 202	4 AMT

## • Setup Prompt Timeout

Setup Prompt Timeout is to delay the BIOS post before entering the operating system for a period of seconds according to user's setting. The default setting is 1.

## Bootup NumLock State

Use this item to select the power-on state for the keyboard NumLock. The default setting is On.

Quiet Boot

Quiet boot is a boot process that is performed without displaying diagnostic or status information on the screen. The default setting is On. The default setting is disable.

Network Stack

Enable or disable UEFI Network stack.

• Boot Option Priorities [Boot Option #1, Boot Option #2] These are settings for boot priority. Specify the boot device priority sequence from the available devices.

## 4.8 Save & Exit Menu

The Save & Exit menu allows users to load system configurations with optimal or fail-safe default values.

Aptio Setup - AMI	
Main Advanced Chipset Security Boot Save & Exit	
Save Options Save Changes and Exit Discard Changes and Exit Save Changes and Reset Discard Changes Discard Changes Default Options Restore Defaults Save as User Defaults Restore User Defaults Boot Override Windows Boot Manager (EMMC IMO64G) UEFI: TOSHIBA TransMemory 1.00, Partition 1 (TOSHIBA TransMemory 1.00)	<pre>→+: Select Screen 11: Select Item Enter: Select +/-: Change Opt. F1: General Help F2: Previous Values F3: Optimized Defaults F4: Save &amp; Exit ESC: Exit</pre>
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## • Save Changes and Exit

When you have completed the system configuration changes, select this option to leave Setup and continue to boot to operating system. Select Save Changes and Exit from the Save & Exit menu and press <Enter>. Select Yes to save changes and exit.

## • Discard Changes and Exit

Select this option to quit Setup without making any permanent changes to the system configuration and continue to boot to operating system. Select Discard Changes and Exit from the Save & Exit menu and press <Enter>. Select Yes to discard changes and exit.

## • Save Changes and Reset

When you have completed the system configuration changes, select this option to leave Setup and reboot the computer so the new system configuration parameters can take effect. Select Save Changes and Reset from the Save & Exit menu and press <Enter>. Select Yes to save changes and reset.

## • Discard Changes and Reset

Select this option to quit Setup without making any permanent changes to the system configuration and reboot the computer. Select Discard Changes and Reset from the Save & Exit menu and press <Enter>. Select Yes to discard changes and reset.

## • Save Changes

When you have completed the system configuration changes, select this option to save changes. Select Save Changes from the Save & Exit menu and press <Enter>. Select Yes to save changes.

**已註解 [jeremy4]:** 其他手冊如 DSP 寫 "return to Main Menu",請再確認

**已註解 [jeremy5]:** 其他手冊如 DSP 寫 "return to Main Menu",請再確認

## • Discard Changes

Select this option to quit Setup without making any permanent changes to the system configuration. Select Discard Changes from the Save & Exit menu and press <Enter>. Select Yes to discard changes.

## Restore Defaults

It automatically sets all Setup options to a complete set of default settings when you select this option. Select Restore Defaults from the Save & Exit menu and press <Enter>.

## • Save as User Defaults

Select this option to save system configuration changes done so far as User Defaults. Select Save as User Defaults from the Save & Exit menu and press <Enter>.

## • Restore User Defaults

It automatically sets all Setup options to a complete set of User Defaults when you select this option. Select Restore User Defaults from the Save & Exit menu and press <Enter>.

## Boot Override

Select a drive to immediately boot that device regardless of the current boot order.

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# Appendix A Watchdog Timer

## A.1 About Watchdog Timer

Software stability is a major issue in most application. Some embedded systems are not watched by human for 24 hours. It is usually too slow to wait for someone to reboot when computer hangs. The systems need to be able to reset automatically when things go wrong. The watchdog timer gives us solutions in this regard.

The watchdog timer is a counter that triggers a system reset when it counts down to zero from a preset value. The software starts the counter with an initial value and must reset it periodically. If the counter ever reaches zero which means the software has crashed, the system will reboot.

## A.2 How to Use Watchdog Timer

```
#include <stdio.h>
#include <conio.h>
#include <bios.h>
#define UINT unsigned int
#define NCT5104I0
                            0x2E
int main()
{
   UINT WDT mode = 0;
                        // 00H for second, 08h for minute
   UINT WDT_Count = 0;
                           // Range from 00 to FF
   //Un-lock super I/O
   outp( NCT5104I0, 0x87 );
   outp( NCT5104I0, 0x87 );
   //Select Logic device
   outp( NCT5104I0, 0x07 );
   outp( NCT5104I0+1, 0x08 );
   //Enable WDT
   outp( NCT5104I0, 0x30 );
   outp( NCT5104I0+1, 0x01 );
   //Set Count mode
   outp( NCT5104I0, 0xF0 );
   outp( NCT5104I0+1, WDT_mode );
```

Watchdog Timer

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```
//WDT counting re-set timer
outp( NCT5104IO, 0xF1 );
outp( NCT5104IO+1, WDT_Count );
// IF to disable WDT:
//outp( NCT5104IO, 0x30 );
```

//outp( NCT5104I0, 0x30 ); //outp( NCT5104I0+1, 0x00 );

return 0;

}

Watchdog Timer